
ANNOUNCEMENTS

New Executive Committee

An election for a new Executive Committee was held at the New Orleans CACHE meeting in March, 1998. The following CACHE Trustees were elected to the Executive Committee and will assume their duties on July 1, 1998.

President

Jim Davis, Ohio State University

Jim Davis is Professor of Chemical Engineering at Ohio State University. He also holds the position of Associate Provost and Director of University Technology Services with responsibility for strategic planning in information technologies and for central computing, technology enhanced learning and research coordination and technology support operations. He came to Ohio State University with industrial experience at Amoco Chemicals Corporation and Argonne National Laboratory. Since the early 1980s, Jim's research work has been focused in the area of Intelligent Systems in process operations and design. The work has pushed on the structure of process knowledge and has led to the implementation of a number of intelligent decision support systems in a variety of industrial applications. Jim has made numerous presentations and published extensively in the area. In addition he has consulted widely in the areas of knowledge-based systems and neural networks. Jim has been a CACHE trustee for almost 10 years and has served on the executive committee for the past 4 years. Most recently, he has been particularly active with the curriculum committee and the administration of the CACHE computing surveys. Jim has also been active in the AIChE for many years including area programming activities, chairing technical sessions and CAST Director from 1990-93. Jim is looking forward to a continued role on the CACHE executive committee as President and as CACHE responds to the changing impact of computing technology in Chemical Engineering education.

Vice President

Mike Doherty, University of Massachusetts

Michael F. Doherty is Professor of Chemical Engineering at the University of Massachusetts at Amherst. He received his B.Sc. in Chemical Engineering from Imperial College, University of London in 1973, and his Ph.D. in Chemical Engineering from Trinity College, University of Cambridge in 1977. He taught at the University of Minnesota before joining

the faculty at the University of Massachusetts in 1977. He has since held a visiting appointment at the University of Minnesota in the Spring Quarter of 1981 and was a visiting scholar at the University of California at Berkeley for the 1984 calendar year. His research interests include design and synthesis of nonideal separation systems, separation with chemical reaction, and crystallization of organic materials (especially the relationships between lattice structure, processing conditions and crystal morphology). He has published over 100 technical papers and was awarded (jointly with M.F. Malone and Z.T. Fidkowski) best paper of the year for 1993 by the editors of Computers and Chemical Engineering. In 1996 he was awarded (jointly with M. F. Malone) the Computing in Chemical Engineering Award of the AIChE CAST Division. He has served as a consultant for many companies in the area of separations technology. Dr. Doherty was Head of Department from 1989-1997, and currently serves as Director of the Center for Process Design and Control at the University of Massachusetts. He has been a Trustee of the CACHE Corporation since 1987. 1994 he co-chaired with L. T. Biegler the CACHE conference FOCAPD 94. In 1993 he was Chair of the Computing and Systems Technology Division of the AIChE. He serves as a member of the Editorial Boards for Computers and Chemical Engineering (1997-2002), Process Systems Engineering Series, Academic Press (1997-), and Industrial and Engineering Chemistry Research (1995-1998).

Secretary

Peter Rony, Virginia Tech

Peter Rony is Professor of Chemical Engineering at Virginia Polytechnic Institute and State University in Blacksburg, Virginia, where he has taught since 1972. Appalachia-located Blacksburg, through the efforts of Jonathan Titus and Rony, was a pioneering community in microprocessor breadboarding, microprocessor trainers, and microprocessor education. During the 1970s and early 1980s, he authored or co-authored ten laboratory-based textbooks on the subjects of digital electronics, microprocessor interfacing, microprocessor programming, and introductory robotics. Selected books were translated into Italian, Spanish, French, German, and Japanese.

ANNOUNCEMENTS

Bugbook III, on 8080A microprocessor programming and interfacing (August 1975), was the first faculty-authored textbook on microprocessors in the U.S.

Rony has been a trustee of CACHE since 1981. As a CACHE trustee, he has focused on the evolving technology of microcomputers through his participation in the following activities:

1. A CACHE-supported short course on microprocessor interfacing and programming (1981 to 1982).
2. Chairman of the CACHE Email Task Force. (1984 to 1996)

NOTE: The Email task force conducted early (mostly unsuccessful) experiments in the use of email in chemical engineering education — based upon the use of ITT Comppmail+, IBM Grand at Louisiana State University (with Dan Reible), and BITNET. For a history, please consult the chapter, “CACHE and The Use of Computer Networks for Education” in Computers in Chemical Engineering Education, Brice Carnahan, Editor, 1996.

3. Chairman of the CACHE ad-hoc task force on CD-ROM technology. (1994 to 1997)

NOTE: The task force produced the 25th Anniversary CACHE CD-ROM (November 1994), the Volume 2 CACHE CD-ROM (April 1996), and the Chicago AIChE National Meeting CD-ROM (November 1996).

Peter has been the editor, since 1986, of CAST Communications, a divisional newsletter of the AIChE. He was editor of the IEEE Computer Society magazine, IEEE MICRO, from 1983 to May 1985. He has taught, worldwide, over 60 short courses on digital electronics and microprocessor interfacing.

Peter earned a B.S.ChE degree from the California Institute of Technology in 1960 and a Ph.D. in Chemical Engineering from the University of California at Berkeley in 1965. He has just co-authored, with his sister Ellen, a book, The Domain Name Handbook. High Stakes and Strategies in Cyberspace which includes a bound, archival CD-ROM disc – with 3676+ pages of archival text — and an associated web site located at URL <http://www.domainhandbook.com>.

New Academic Trustee

Hert Britt, Aspen Technology, Inc.

Herbert I. Britt currently serves as Chief Technology Officer for Aspen Technology, Inc., based in Cambridge, Massachusetts. Herb began his career in computer-aided process engineering in 1973, working on the IPES process simulator at Union Carbide. In 1977 he joined the MIT Aspen Project where he served as Associate Project Manager and led the development of the ASPEN process simulator. He was a founder of Aspen Technology in 1981, serving until recently as Senior Vice President in charge of ASPEN PLUS, SPEEDUP and AspenTech's other simulation products. Current research interests include open component architectures and integrated environments for computer-aided process engineering, design, and operations.

Herb has been active in AIChE. He has served as a Director and as Secretary/Treasurer of CAST, has chaired several sessions, was a founder and the first Vice-Chairman of pdXi (Process Data Exchange Institute), and in 1995 received the CAST Computing Practice Award. He is currently on the Editorial Board of Computers and Chemical Engineering. Herb is active in the CAPE-OPEN project to define open standards for the plug-and-play interoperability of simulation components.

Herb received B.S. and Ph.D. degrees in Chemical Engineering from the University of Missouri. Herb is known for the Boston-Britt flash algorithm and the Britt-Luecke maximum likelihood estimation method.